H

I

IU

n

19. (Amended) An electronic instrument provided with the semiconductor device

infloper

as defined in claim 1.

27. (Amended) The method of fabricating a semiconductor device as defined in claim 20, further comprising

a step of forming a recognition hole in the substrate at a position differing from the holes, and forming a recognition pattern over the recognition hole on the side of a surface of the substrate including the interconnecting pattern.

29. (Amended) The method of fabricating a semiconductor device as defined in claim 20,

wherein the conductive members are a plurality of layered bumps.

<u>REMARKS</u>

Claims 1-32 are pending. By this Preliminary Amendment, claims 7, 10, 17-19, 27 and 29 are amended to eliminate multiple dependencies. Prompt and favorable consideration on the merits is respectfully requested.

The attached Appendix includes marked-up copies of each rewritten claim (37 C.F.R. 1.121(c)(1)(ii)).

Respectfully submitted,

Registration No. 27,075

Joel S. Armstrong Registration No. 36,430

JAO: JSA/zmc

Attached: APPENDIX Date: May 30, 2001

OLIFF & BERRIDGE, PLC P.O. Box 19928 Alexandria, Virginia 22320 Telephone: (703) 836-6400 DEPOSIT ACCOUNT USE AUTHORIZATION Please grant any extension necessary for entry; Charge any fee due to our Deposit Account No. 15-0461